



Nanoelectronic Mixed-Signal System Design

Saraju Mohanty

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Cutting-edge nanoelectronic mixed-signal system design methods

Written by the director of the NanoSystem Design Laboratory at the University of North Texas, this authoritative resource discusses mixed-signal circuit and system design based on existing and emerging nanoelectronic technologies. The book features coverage of both digital and analog applications using nanoscale CMOS and post-CMOS. Key techniques required for design for excellence and manufacturability are discussed in this practice-driven text.

Nanoelectronic Mixed-Signal System Design covers:

- Opportunities and challenges of nanoscale technology and systems
- Emerging systems designed as analog/mixed-signal system-on-chips (AMS-SoCs)
- Nanoelectronics issues in design for excellence
- Phase-locked loop component circuits
- Electronic signal converter circuits
- Sensor circuits and systems
- Memory in the AMS-SoCs
- Mixed-signal circuit and system design flow
- Mixed-signal circuit and system simulation
- Power-, parasitic-, and thermal-aware AMS-SoC design methodologies
- Variability-aware AMS-SoC design methodologies
- Metamodel-based fast AMS-SoC design methodologies



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